

switching devices for switching the data signals to be applied from the data lines to the liquid crystal cells in response to signals on the gate lines, the circuit comprising:

a voltage supply for generating a gate voltage required for the gate lines;

a gate line driver for applying the gate voltage from the voltage supply to the gate lines to drive the gate lines; and

A<sub>1</sub> a current controller including a resistor and a thermistor for responding to a change in the ambient temperature to change an amount of current of the gate voltage to be applied from the voltage supply to the gate line driver.

2. (Amended) The charge characteristic compensating circuit as claimed in claim 1, wherein said resistor and said thermistor are connected, in parallel, between the voltage supply and the gate line driver.

3. (Amended) The charge characteristic compensating circuit as claimed in claim 1, wherein said resistor and said thermistor are connected, in series, between the voltage supply and the gate line driver.

A<sub>2</sub> 6. (Amended) A charge characteristic compensating circuit for a liquid crystal display panel including a plurality of liquid crystal cells arranged at each intersection between data lines and gate lines to control a light transmissivity in response to data signals from the data lines, and a plurality of switching devices for switching the data signals to be applied from the data lines to the liquid crystal cells in response to signals on the gate lines, the circuit, comprising:

A2 a voltage supply for generating a gate voltage required for the gate lines;  
a gate line driver for applying the gate voltage from the voltage supply to the gate lines to drive the gate lines; and  
a current controller including a resistor and a thermistor for responding to a change in the ambient temperature to change a voltage level of the gate voltage to be applied from the voltage supply to the gate line driver.

7. (Amended) The charge characteristic compensating circuit as claimed in claim 6, wherein the current controller includes a resistive voltage divider connected between the voltage supply and the gate line driver and composed of said resistor and said thermistor.

9. (Amended) A charge characteristic compensating circuit for a liquid crystal display panel (LCD), comprising:

A3 a voltage converter generating a high level gate voltage;  
a gate line controller including a resistor and a thermistor receiving said high level gate voltage from said voltage converter and supplying a controlling signal that varies as an ambient temperature varies; and  
a gate line driver receiving said controlling signal from said gate line controller and driving a gate line.

A4 12. (Amended) The charge characteristic compensating circuit of claim 11, wherein said current controller includes said thermistor.

A5 14. (Amended) The charge characteristic compensating circuit of claim 13, wherein said current controller further includes said resistor such that said resistor is in one of a parallel connection or a serial connection with said thermistor.

A6 17. (Amended) The charge characteristic compensating circuit of claim 16, wherein said voltage divider includes said thermistor.

A7 19. (Amended) The charge characteristic compensating circuit of claim 18, wherein said voltage divider further includes said resistor such that said resistor is connected between said voltage converter and an input to said gate line driver and said negative temperature coefficient thermistor is connected between ground and said input to said gate line driver.

A8 22. (Amended) A method to compensate for a charge characteristic of a liquid crystal display panel (LCD), comprising:  
supplying a controlling signal that varies by way of a resistor and thermistor as an ambient temperature varies; and  
driving a gate line according to said controlling signal.